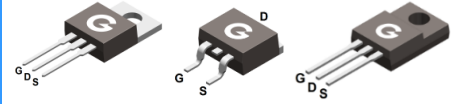
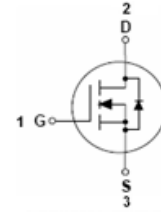


Features

- Planar technology
- Low on-resistance
- Low gate charge
- Low reverse transfer capacitances
- HBM: JESD22-A114-B: 1C

HF



TO-220AB TO-263 ITO-220AB

Mechanical Data

- Case: TO-220AB, TO-263, ITO-220AB
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin-Plated Leads, Solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL3N1K2	TO-220AB	50 pcs / Tube	3N1K2
BL3N1K2B	TO-263	50 pcs / Tube or 800 pcs / Tape & Reel	3N1K2B
BL3N1K2F	ITO-220AB	50 pcs / Tube	3N1K2F

Maximum Ratings (@ $T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	1200	V
Gate-to-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current ($T_c = 25^\circ\text{C}$)	I_D	3	A
Continuous Drain Current ($T_c = 100^\circ\text{C}$)		1.9	
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_c = 25^\circ\text{C}$)	I_{DM}	12	A
Single Pulse Avalanche Energy ²	E_{AS}	30	mJ
Power Dissipation (TO-220AB, $T_c = 25^\circ\text{C}$)	P_D	200	W
Power Dissipation (TO-263, $T_c = 25^\circ\text{C}$)		200	
Power Dissipation (ITO-220AB, $T_c = 25^\circ\text{C}$)		33	
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	TO-220AB/TO-263	ITO-220AB	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	0.62	3.8	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air	$R_{\theta JA}$	50	62.5	$^\circ\text{C/W}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	1200	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 1200V, V_{GS} = 0V$	-	-	25	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	± 100	nA
On Characteristics						
$R_{DS(ON)}$	Drain-Source On-resistance ^{*1}	$V_{GS} = 10V, I_D = 1.5A$	-	5.5	7.5	Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	3.5	5	V
R_G	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	2	-	Ω
Dynamic Characteristics						
C_{ISS}	Input Capacitance	$V_{GS} = 0V$	-	1728	-	pF
C_{OSS}	Output Capacitance	$V_{DS} = 25V$	-	101	-	
C_{RSS}	Reverse Transfer Capacitance	$f = 1.0MHz$	-	8.9	-	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time ^{*3}	$V_{DD} = 750V$ $V_{GS} = 10V$ $I_D = 3A$ $R_G = 4.7\Omega$	-	25	-	ns
t_r	Turn-on Rise Time ^{*3}		-	48	-	
$t_{d(OFF)}$	Turn-Off Delay Time ^{*3}		-	57	-	
t_f	Turn-Off Fall Time ^{*3}		-	52	-	
Q_G	Total Gate-Charge	$V_{DD} = 960V$	-	45	-	nC
Q_{GS}	Gate to Source Charge	$V_{GS} = 10V$	-	7.5	-	
Q_{GD}	Gate to Drain ("Miller") Charge	$I_D = 3A$	-	21.2	-	
Source-Drain Diode Characteristics						
V_{SD}	Diode Forward Voltage ^{*1}	$I_{SD} = 3A, V_{GS} = 0V$	-	0.8	1.5	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F = 1.5A, V_R = 100V$	-	1	-	μs
Q_{rr}	Body Diode Reverse Recovery Charge	$di/dt = 100A/\mu s$	-	6.6	-	μC

Notes:

1. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
2. The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 50V, V_{GS} = 15V, L = 10mH$
3. Guaranteed by design, not subject to production

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

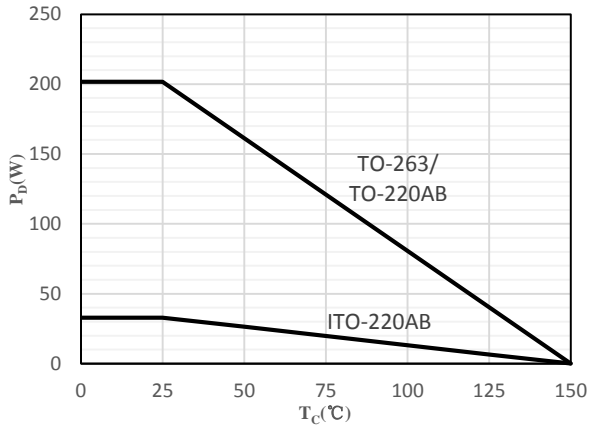


Fig 1 Power Dissipation

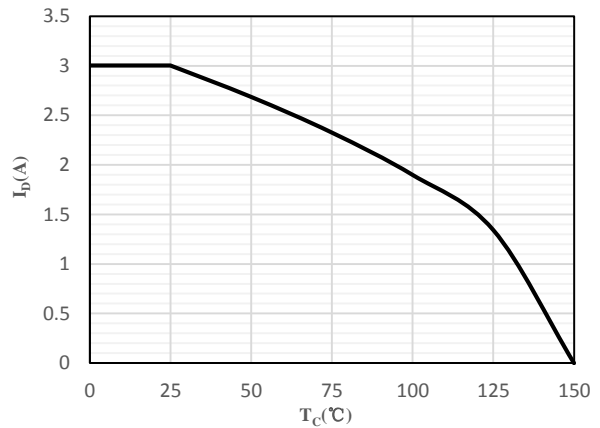


Fig 2 Drain Current

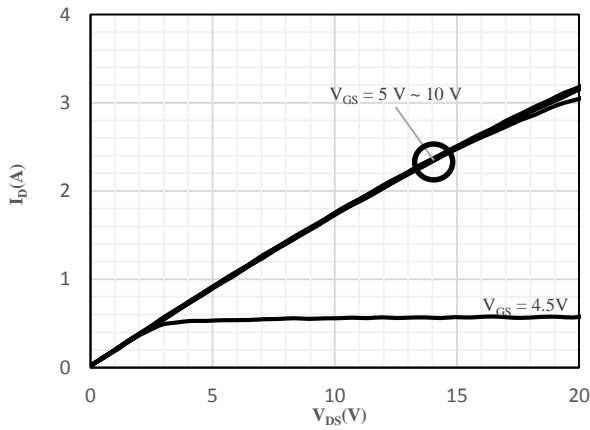


Fig 3 Typical Output Characteristics

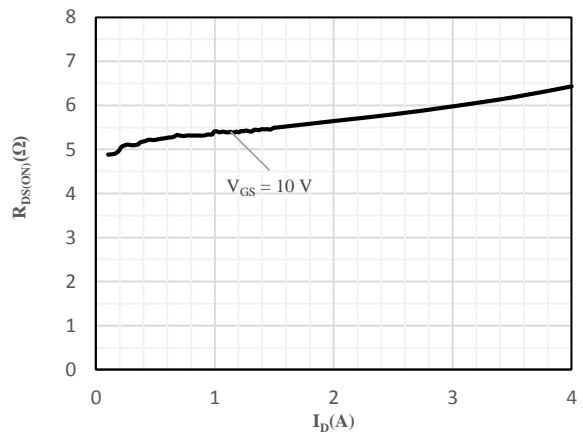


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

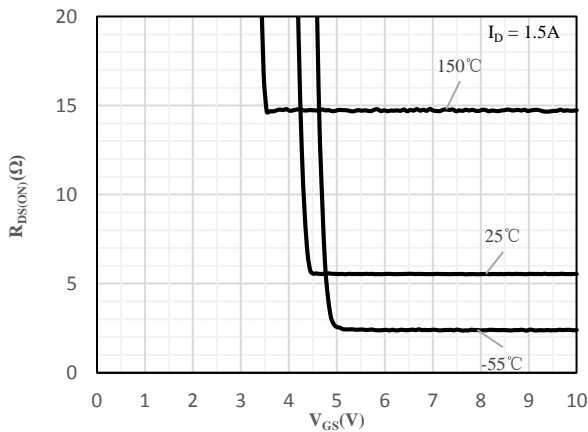


Fig 5 On-Resistance vs. Gate-Source Voltage

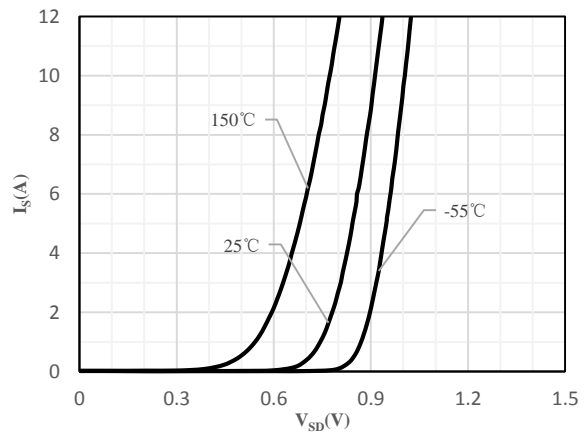


Fig 6 Body-Diode Characteristics

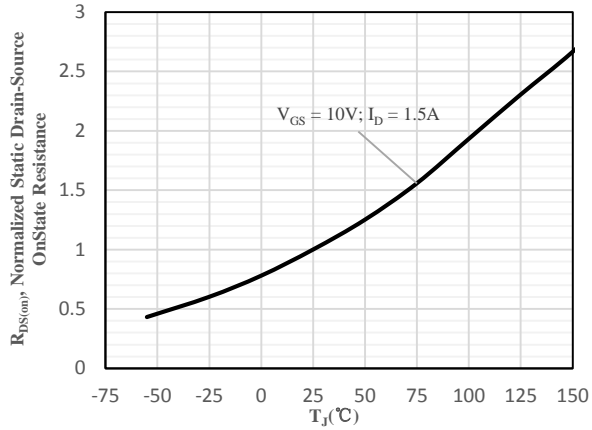


Fig 7 Normalized On-Resistance vs. Junction Temperature

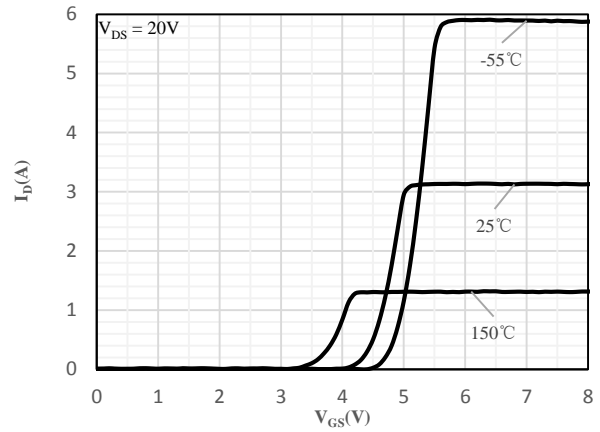


Fig 8 Transfer Characteristics

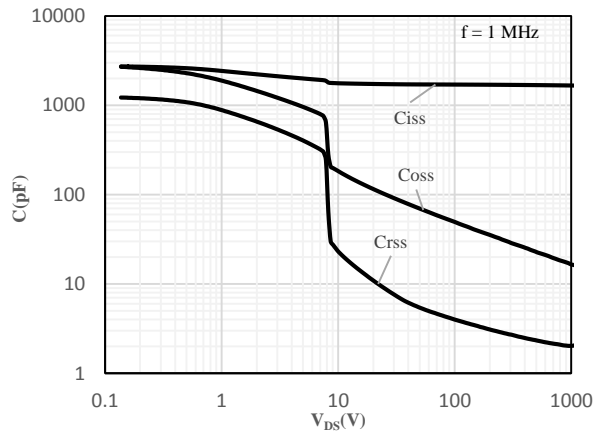


Fig 9 Capacitance Characteristics

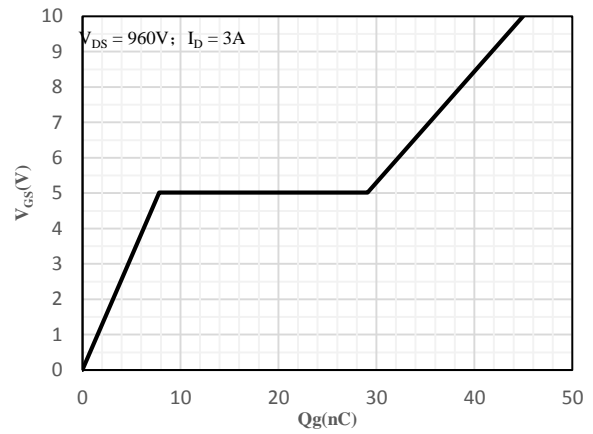


Fig 10 Gate-Charge Characteristics

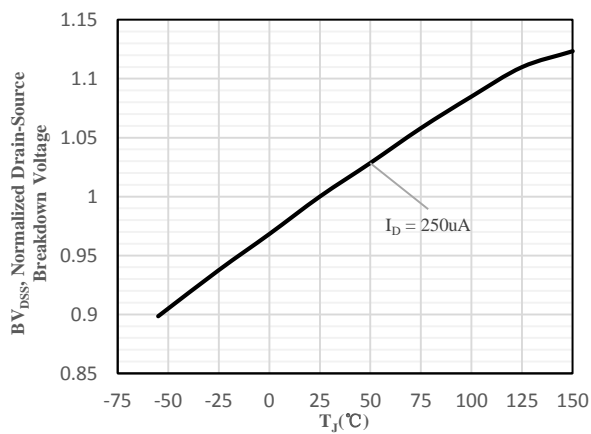


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

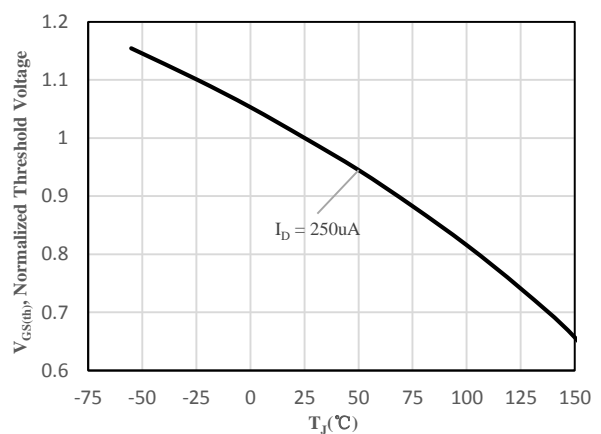


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

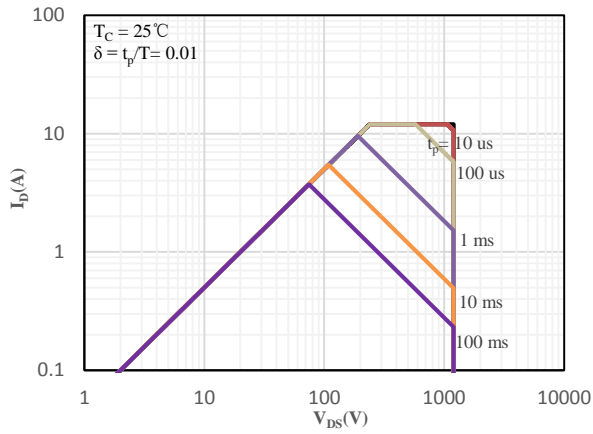


Fig 13 Safe Operating Area (TO-220AB / TO-263)

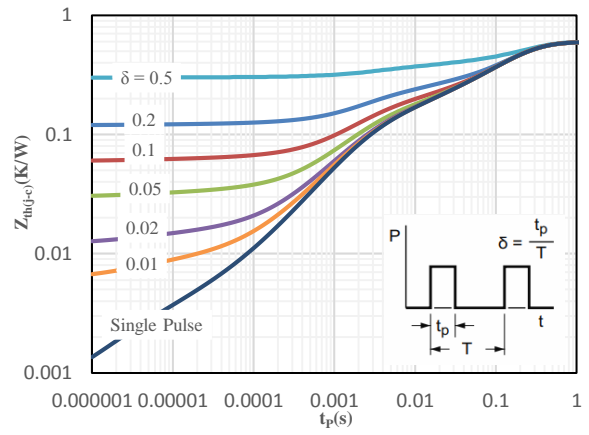
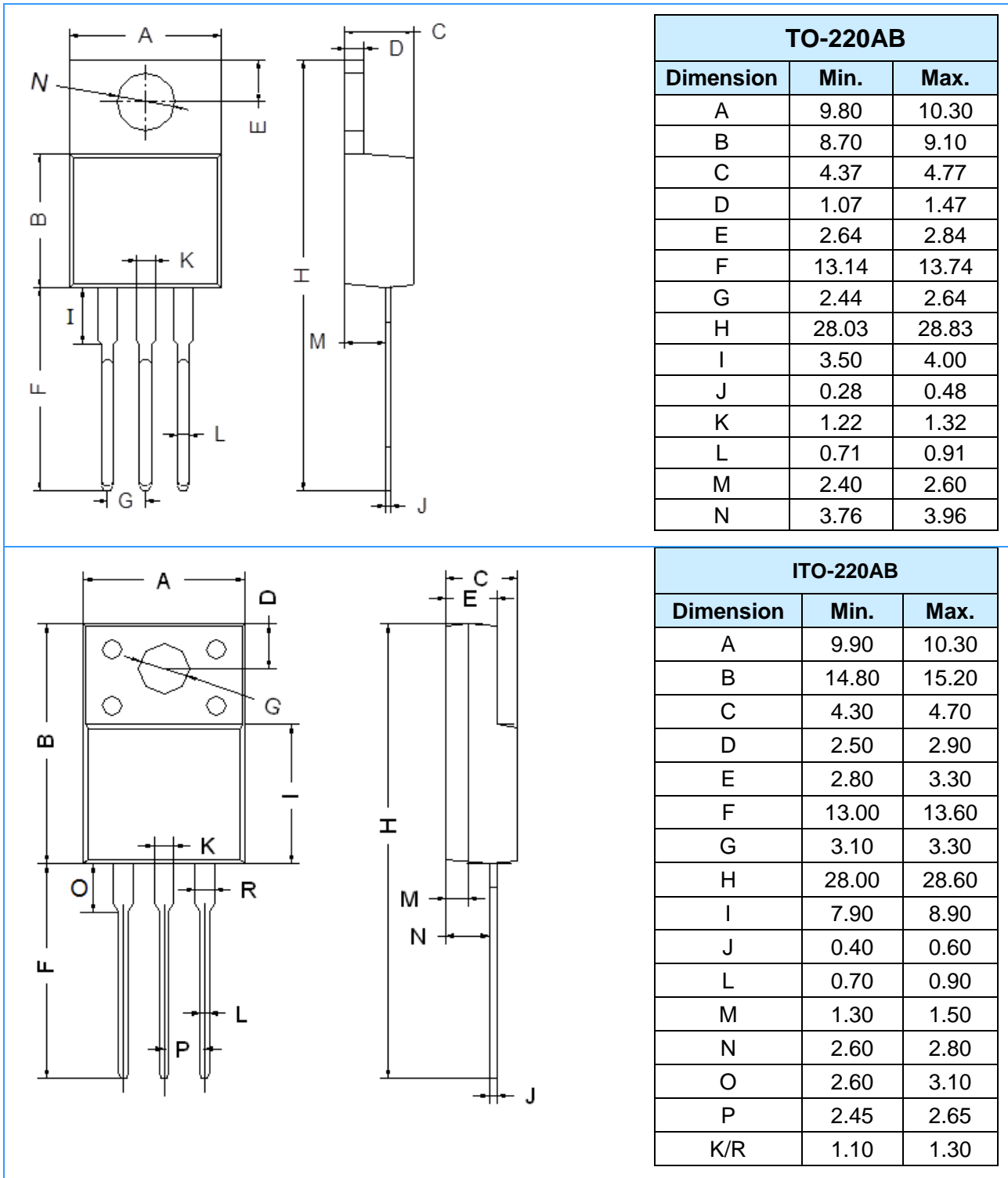
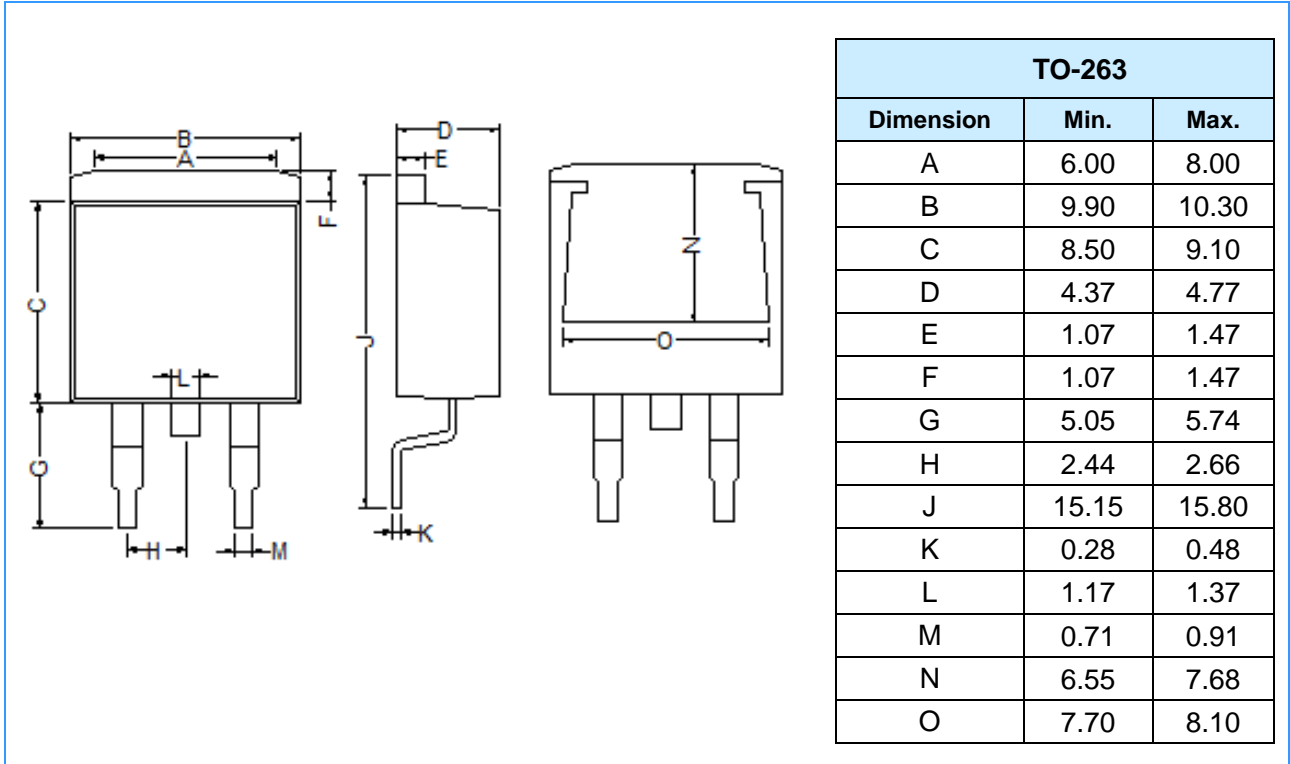


Fig 14 Maximum transient thermal impedance (TO-220AB / TO-263)

Package Outline Dimensions (Unit: mm)





Mounting Pad Layout (Unit: mm)

